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**Seventh Semester B.E. Degree Examination, Dec.2015/Jan.2016**

**Embedded Computing System**

Time: 3 hrs.

Max. Marks:100

**Note: Answer FIVE full questions, selecting at least TWO questions from each part.**

**PART – A**

- 1
  - a. What is an Embedded Computing system? Mention its characteristics. (04 Marks)
  - b. Explain Embedded system Design process with respect to GPS moving map. (10 Marks)
  - c. Draw and explain the sequence diagram for transmitting a control input in a model train controller. (06 Marks)
  
- 2
  - a. Write ARM assembly code to implement the following C assignments
    - i)  $x = (a - b) + (c * d)$  ;
    - ii)  $y = (a < 3) | (b \& 1b)$ ; (06 Marks)
  - b. Explain the pipelined execution of a branch in ARM using a pipeline diagram. (04 Marks)
  - c. What is a cache? Explain the following with diagram
    - i) Two – Level cache system
    - ii) Direct – Mapped cache
    - iii) Set – Associative cache (10 Marks)
  
- 3
  - a. Draw the UML state diagram of bus bridge operation and explain. (06 Marks)
  - b. Explain with a neat diagram, the bus with a DMA controller. (06 Marks)
  - c. Write a requirement table for an Alarm clock. (08 Marks)
  
- 4
  - a. Briefly explain Control/Data Flow Graphs. Draw the CDFG for the C codes given below
    - i)
 

```

proc1( );
  If(a < b)
    proc2( );
  else
    proc3( )
  proc4 ( );
  switch(op)
  {
    case 1 : proc5( );
              break;
    case 2 : proc6( );
              break;
    case 3 : proc7( );
              break;
  }
Proc8( )

```
    - ii) for (i=0; i<N;i++)
 

```

{
  a = proc1(a,b);
  b =proc2 (a,b);
}

```

(06 Marks)

Important Note : 1. On completing your answers, compulsorily draw diagonal cross lines on the remaining blank pages.  
2. Any revealing of identification, appeal to evaluator and /or equations written eg. 42+8 = 50, will be treated as malpractice.

**10CS72**

- b. Show the contents of the Assembler's symbol Table at the end of code generation for each line of the following program. **(10 Marks)**
- i) ORG 100  
 P1 CMP r0, r1  
     BEQ P1  
 P2 CMP r0, r2  
     BEQ P2  
 P3 CMP r0, r3  
     BEQ P3
- ii) ORG 200  
 P1 ADR r4, a  
     LDR r0, [r4]  
     ADR r4, e  
     LDR r1, [r4]  
     ADD r0, r0, r1  
     BNE q1  
 P2 ADR r4, e
- c. Explain briefly different types of performance measures on programs. **(04 Marks)**

**PART – B**

- 5 a. What is Real-Time operating system and Real-Time Kernel? Define Task Control Block (TCB) and describe the structure of a TCB. **(07 Marks)**
- b. Explain the synchronization issues in resource utilization. Using the Dining Philosopher's problem. Mention the solutions for those issues. **(07 Marks)**
- c. Three processes with process IDs P1, P2, P3 with estimated completion time 8, 5, 4 milliseconds respectively, enters the ready queue together in the order P2, P3, P1. Process P4 with estimated execution time 4 milliseconds entered the 'Ready' queue 3 milliseconds later the start of execution of P1. Calculate the waiting time and Turn Around Time (TAT) for each process and the Average waiting time and Average Turn Around time (Assuming there is no I/O waiting for the processes in RR algorithm with Time slice = 2ms). **(06 Marks)**
- 6 a. Explain briefly the concept of counting semaphore and Mutex. **(08 Marks)**
- b. What is advanced configuration and power interface? Explain the basic global power states supported by ACPI. **(06 Marks)**
- c. Describe how to evaluate OS performance in terms of the following:  
 i) Context switching  
 ii) Cache scheduling **(06 Marks)**
- 7 a. With a neat diagram, explain the various fields of CAN frame. **(07 Marks)**
- b. Explain with a neat diagram, the structure of an IP packet. **(07 Marks)**
- c. List and explain the advantages and limitations of simulator Based Debugging. **(06 Marks)**
- 8 a. With a neat diagram, explain elements of the ARM AMBA bus system. **(05 Marks)**
- b. Write a short note on Logic Analyzer. **(05 Marks)**
- c. Explain with a diagram the concept of Context switching, context saving and context Retrieval. **(05 Marks)**
- d. Differentiate Non-preemptive SJF scheduling algorithm and Preemptive SJF scheduling algorithm with simple examples. **(05 Marks)**

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